

II. AMENDMENTS

Please amend the Specification as follows:

Continuation Data:

This is a continuation-in-part (CIP) of U.S. Patent Application Serial No. 09/239,659 titled "Bandwidth Reducing Memory Controller Including Scalable Embedded Parallel Data Compression and Decompression Engines" and filed January 29, 1999, whose inventors are Thomas A. Dye, Manuel J. Alvarez II, and Peter Geiger, which is a continuation-in-part (CIP) of U.S. Patent Application Serial No. 08/916,464, filed August 8, 1997, now U.S. Patent No. 6,173,381, issued January 1, 2001, titled "Memory Controller Including Embedded Data Compression And Decompression Engines," whose inventor is Thomas A. Dye.

Please cancel claims 39-40, 42-44, and 46-54. Please amend claims 1, 17, 41, and 45, as follows:

Claims:

1. (Currently amended) A method for managing memory in a system including one or more memory modules, wherein at least one of the one or more memory modules includes a decompression engine, the method comprising:
 - storing compressed data on the one or more memory modules;
 - ~~a device~~ initiating a read of requested data from the one or more memory modules, wherein the requested data comprises compressed requested data stored on the one or more memory modules in a compressed format;
 - ~~the one or more memory modules decompressing, through the one or more memory modules,~~ the compressed requested data to produce uncompressed requested data using parallel decompression, wherein said decompressing the compressed requested data comprises: